



09/510,375

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Brett L. Williams	Examiner:	Hong Chong Kim
Serial No.:	09/510,375	Group Art Unit:	2186
Filed:	February 22, 2000	Docket:	303.164US3
Title:	SYSTEM SUPPORTING MULTIPLE MEMORY MODES INCLUDING A BURST EXTENDED DATA OUT MODE		

INFORMATION DISCLOSURE STATEMENT

MS RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement.

INFORMATION DISCLOSURE STATEMENT

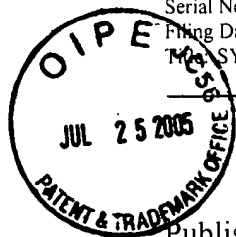
Serial No :09/510,375

Filing Date: February 22, 2000

MS SYSTEM SUPPORTING MULTIPLE MEMORY MODES INCLUDING A BURST EXTENDED DATA OUT MODE

Page 2

Dkt: 303.164US3



Pursuant to 37 C.F.R. 1.98(a)(2), Applicant believes that copies of cited U.S. Patents and Published Applications are no longer required to be provided to the Office. Notification of this change was provided in the United States Patent and Trademark Office OG Notices dated October 12, 2004. Thus, Applicant has not included copies of any US Patents or Published Applications cited with this submission. Should the Office require copies to be provided, Applicant respectfully requests that notice of such requirement be directed to Applicant's below-signed representative. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

BRETT L. WILLIAMS

By his Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938
Minneapolis, MN 55402
(612) 349-9587

Date

12 July '05

By

Timothy B Clise
Reg. No. 40,957

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 21 day of July, 2005.

Name

Tina Kohut

Signature

Z.UA

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	09/510,375
Filing Date	February 22, 2000
First Named Inventor	Williams, Brett
Group Art Unit	2186
Examiner Name	Kim, Hong

Set 1 of 3

Attorney Docket No: 303.164US3

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
	US-2001/0032290A1	10/18/2001	Williams, Brett I.	02/22/2000
	US-2002/0001234	01/03/2002	Merritt, Todd A., et al.	07/27/1999
	US-2002/0196668	12/26/2002	Merritt, Todd A., et al.	08/29/2002
	US-2002/0133665	09/19/2002	Mailloux, Jeffrey S., et al.	12/03/1997
	US-2003/0002336	01/02/2003	Merritt, Todd A., et al.	08/29/2002
	US-2003/0002350	01/02/2003	Merritt, Todd A., et al.	08/29/2002
	US-2003/0063502	04/03/2003	Merritt, Todd A., et al.	08/29/2002
	US-2003/0063512	04/01/2003	Takahashi, Hiroyuki, et al.	10/09/2002
	US-2003/0067817A1	04/10/2003	Merritt, Todd A., et al.	08/29/2002
	US-4,519,028	05/21/1985	Olsen, Richard E., et al.	02/17/1981
	US-4,594,587	06/10/1986	Chandler, Gregg, et al.	08/30/1983
	US-4,766,431	08/23/1988	Kobayashi, Y., et al.	09/05/1985
	US-4,851,990	07/25/1989	Johnson, William M., et al.	02/09/1987
	US-4,857,899	08/15/1989	Ishii, Takatoshi	12/10/1986
	US-5,146,582	09/08/1992	Begun, Ralph M.	06/19/1989
	US-5,175,835	12/29/1992	Beighe, Edward W., et al.	01/10/1990
	US-5,243,699	09/07/1993	Nickolls, John R., et al.	12/06/1991
	US-5,309,398	05/03/1994	Nagase, , et al.	09/24/1992
	US-5,327,390	07/05/1994	Takasugi, Atsushi	09/15/1993
	US-5,331,471	05/10/1994	Matsumoto, Naoki, et al.	12/07/1992
	US-5,331,593	07/19/1994	Merritt, T. A., et al.	03/03/1993
	US-5,369,622	11/29/1994	McLaury,	04/20/1993
	US-5,426,606	06/20/1995	Takai, Yasuhiro	04/01/1994
	US-5,499,355	03/12/1996	Krishnamohan, K. et al.	11/15/1994
	US-5,587,950	12/24/1996	Sawada, Seiji, et al.	06/05/1995
	US-5,587,964	12/24/1996	Rosich, , et al.	04/01/1996
	US-5,594,704	01/14/1997	Konishi, Yasuhiro, et al.	04/10/1995
	US-5,682,354	10/28/1997	Manning, T.	11/06/1995
	US-5,752,269	05/12/1998	Divivier, Robert J., et al.	05/26/1995
	US-5,812,488	09/22/1998	Zagar, Paul S., et al.	03/05/1997
	US-5,815,462	09/29/1998	Konishi, Yauhiro, et al.	02/12/1997
	US-5,854,911	12/29/1998	Watkins, John E.	07/01/1996
	US-5,966,724	10/12/1999	Ryan, K. J.	01/11/1996
	US-6,006,290	12/21/1999	Suh, Jung Won	11/12/1997
	US-6,381,180	04/30/2002	Merritt, Todd A., et al.	02/26/1998
	US-6,525,971	02/25/2003	Merritt, Todd A., et al.	07/27/1999
	US-6,914,830	07/05/2005	Merritt, T. A., et al.	08/29/2002

EXAMINER**DATE CONSIDERED**

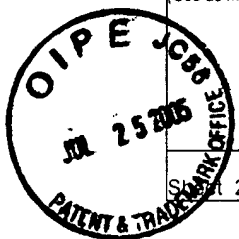
Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. † Applicant's unique citation designation number (optional) ‡ Applicant is to place a check mark here if English language Translation is attached

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

Application Number	09/510,375
Filing Date	February 22, 2000
First Named Inventor	Williams, Brett
Group Art Unit	2186
Examiner Name	Kim, Hong

Attorney Docket No: 303.164US3

Sheet 2 of 3

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	T ²
	EP-0468480	01/29/1992	Takasugi, A.	
	JP-06060658 and English Abstract	03/04/1994	Yamamoto, Hiroshi, et al.	
	JP-5-282859 and English Abstract	10/29/1993	Norihiro, Oookawa	
	JP-5-89663 and English Abstract	04/09/1993	Nagase, et al.	
	JP-6-325599 (no English Abstract available)	11/25/1994	Sannmo, S.	
	JP-6-333393 (no English Abstract available)	12/02/1994	Park, Soon-Kyu	
	JP-61-170994 and English Abstract	08/01/1986	Katsuyuki, S.	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		In: 1996 DRAM Data Book, Micron Technology, Inc. (1996), 1/1 - 1/52	
		In: 1996 DRAM Data Book, Micron Technology, Inc. (1996), pp. 5/75, 7/62	
		Office Action dated June 18, 2003 from related U.S. Application No. 10/232,977 (2003), 7 pgs.	
		Office Action Dated June 4, 2003 from related U.S. Application No. 10/232,092 (2003), 7 pgs.	
		1995 DRAM Data Book, Micron Technology, Inc., (1995), 1-42	
		"4M DRAM", Toshiba American Electronic Components, Inc., (1991), A-137- A-159	
		"Burst DRAM Function & Pinout", Oki Electric Ind. Co., Ltd., 2nd Presentation, Item #619, (September 1994), 5 pgs.	
		"Burst EDO DRAM Information", Micron Technology, Inc., (September 1995), 1-126	
		"Burst Edo Memory Device", PCT Patent Application PCT/US95/16984, (12/22/95)	
		"Burst Edo Memory Device Address Counter", PCT Application PCT/US95/16656, (12/21/95)	
		"Command Truth Table", NEC, (1993), 1 pg.	
		"DRAM 1 Meg x 4 DRAM 5VEDO Page Mode", 1995 DRAM Data Book, Micron Technology, Inc., (1995), 1/1 - 1/30	
		"Hyper page mode DRAM", Electronic Engineering, 66(813), (September 1994), 47-48	
		"Integrated Circuit Technical Data-262, 144 Words x 8 Bits Multiport DRAM", Toshiba Corp., TC52826TS/Z/FT/TR, TEN. Rev.2.1, (1980), 1-63	

EXAMINER**DATE CONSIDERED**

Substitute Disclosure Statement Form (PTO-1449)

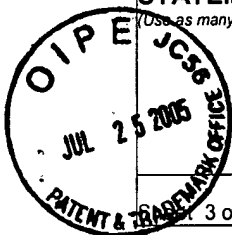
* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



3 of 3

Complete if Known

Application Number	09/510,375
Filing Date	February 22, 2000
First Named Inventor	Williams, Brett
Group Art Unit	2186
Examiner Name	Kim, Hong

Attorney Docket No: 303.164US3

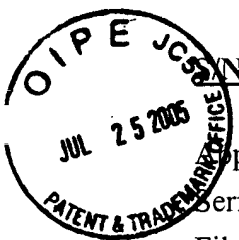
OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		"KM48SV2000 Preliminary CMOS SDRAM", Samsung Electronics, (March 1993), 7-8	
		"Mitsubishi Samples 16M Synch DRAM", <u>Electronic News</u> , (1993), 3-4	
		"Performance: Fast DRAMS can be swapped for SRAM Caches", <u>Electronic Design</u> , vol. 41, no. 15, Cleveland, Ohio, (7/22/93), 55-67	
		"S3 Burst Mode DRAM", <u>S3 Incorporated</u> , Santa Clara, CA, (June 1993), 2 pages	
		"SAMSUNG SYNCHRONOUS DRAM", <u>Samsung Electronics, Revision 1</u> , (March 1993), 1-16	
		"Synchronous DRAM 4 Meg x 4 SDRAM", Micron Semiconductor, Inc., (1983), 1-42	
		MICRON SEMICONDUCTOR, INC., "Synchronous DRAM 2 MEG x 8 SDRAM", Micron Semiconductor, Inc., (April 1994), 1-18	
		TOSHIBA, "Application Specific DRAM", Toshiba American Electronic Components, Inc., (1994), pp. C-178, C-260, C-218	

EXAMINER**DATE CONSIDERED**

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional) ² Applicant is to place a check mark here if English language Translation is attached



IN 09/510,375

PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Brett L. Williams

Examiner: Hong C. Kim

Serial No.: 09/510,375

Group Art Unit: 2186

Filed: February 22, 2000

Docket: 303.164US3

Title: SYSTEM SUPPORTING MULTIPLE MEMORY MODES INCLUDING A
BURST EXTENDED DATA OUT MODECOMMUNICATION CONCERNING RELATED APPLICATION(S)Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicant would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date/Issue Date</u>	<u>Attorney Docket</u>	<u>Title</u>
08/497354 5598376	June 30, 1995	303.165US1	DISTRIBUTED WRITE DATA DRIVERS FOR BURST ACCESS MEMORIES
08/785867 5757703	January 21, 1997	303.165US2	DISTRIBUTED WRITE DATA DRIVERS FOR BURST ACCESS MEMORIES
09/031325 6381180	February 26, 1998	303.165US3	DISTRIBUTED WRITE DATA DRIVERS FOR BURST ACCESS MEMORIES
09/361795 6525971	July 27, 1999	303.165US4	DISTRIBUTED WRITE DATA DRIVERS FOR BURST ACCESS MEMORIES
10/231682 6914830	August 29, 2002	303.165US5	DISTRIBUTED WRITE DATA DRIVERS FOR BURST ACCESS MEMORIES
10/232092	August 29, 2002	303.165US6	DISTRIBUTED WRITE DATA DRIVERS FOR BURST ACCESS MEMORIES
10/232978	August 29, 2002	303.165US7	DISTRIBUTED WRITE DATA DRIVERS FOR BURST ACCESS MEMORIES
10/231942 6728142	August 29, 2002	303.165US8	DISTRIBUTED WRITE DATA DRIVERS FOR BURST ACCESS MEMORIES
10/232977	August 29, 2002	303.165US9	DISTRIBUTED WRITE DATA DRIVERS FOR BURST ACCESS MEMORIES

COMMUNICATION CONCERNING RELATED APPLICATIONS

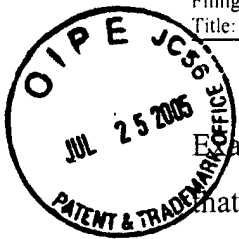
Serial Number: 09/510,375

Filing Date: February 22, 2000

Title: SYSTEM SUPPORTING MULTIPLE MEMORY MODES INCLUDING A BURST EXTENDED DATA OUT MODE

Page 2

Dkt: 303.164US3



Continuations and divisionals may be later filed on the cases listed above, or cited to the Examiner in any previous Communication Concerning Related Applications. Applicants request that the Examiner review all continuations and divisionals of the above-listed or previously-cited patent applications before allowing the claims of the present patent application.

Respectfully submitted,

BRETT L. WILLIAMS

By Applicant's Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938
Minneapolis, MN 55402
(612) 349-9587

Date

12 July '05

By

Timothy B. Clise
Reg. No. 40,957

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 21 day of July, 2005.

Name

Tina Kohout

Signature

2.11.05